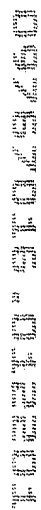


Fig. 1



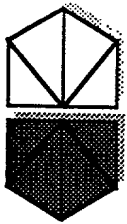
# VG4000<sup>+</sup> Block Diagram



## VG 4000<sup>+</sup> ARCHITECTURE

Fig. 2





# System Controller I/O Card

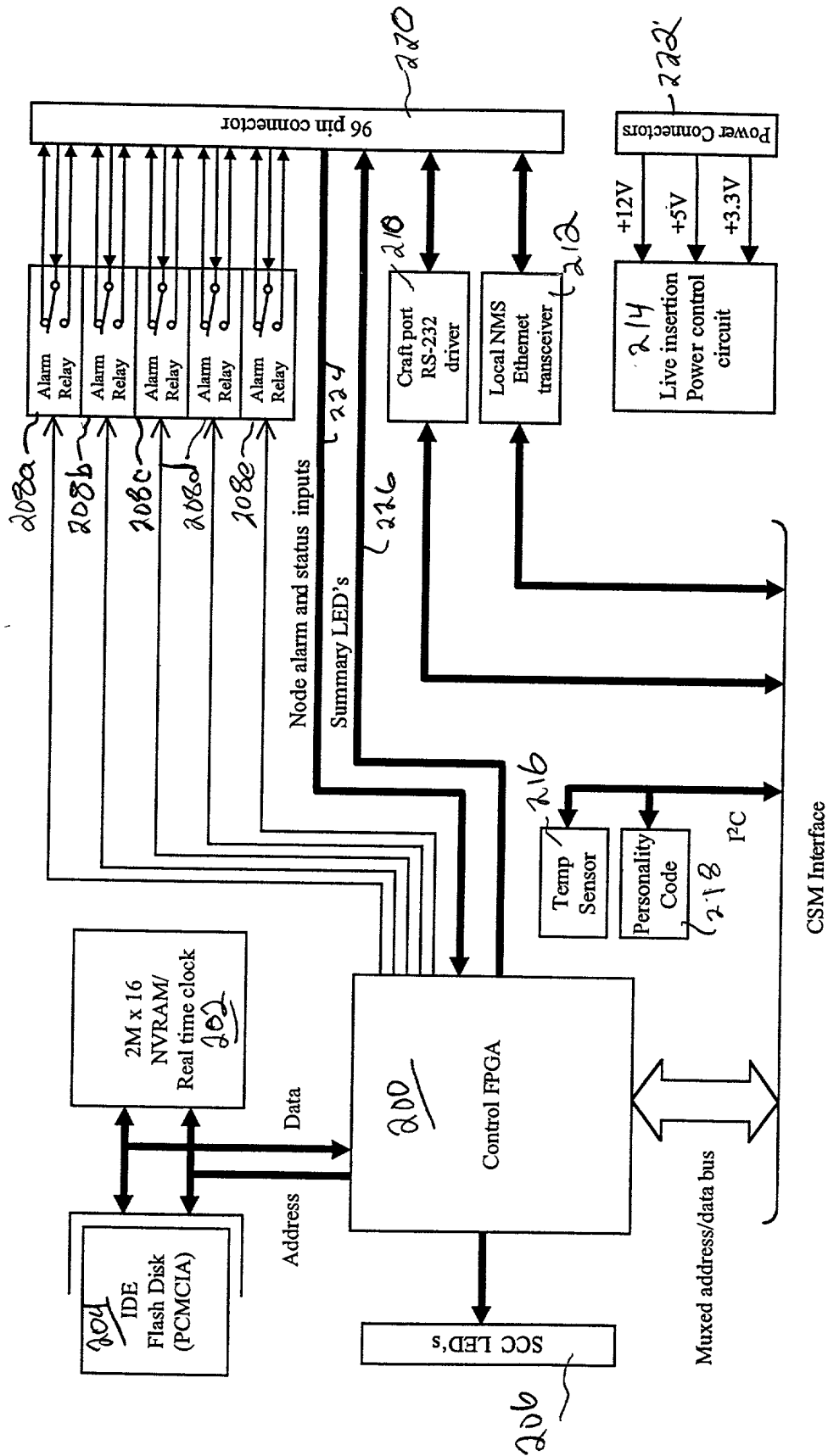
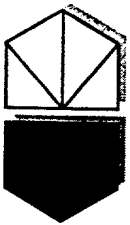


Fig. 4



# Quad OC3 Block Diagram

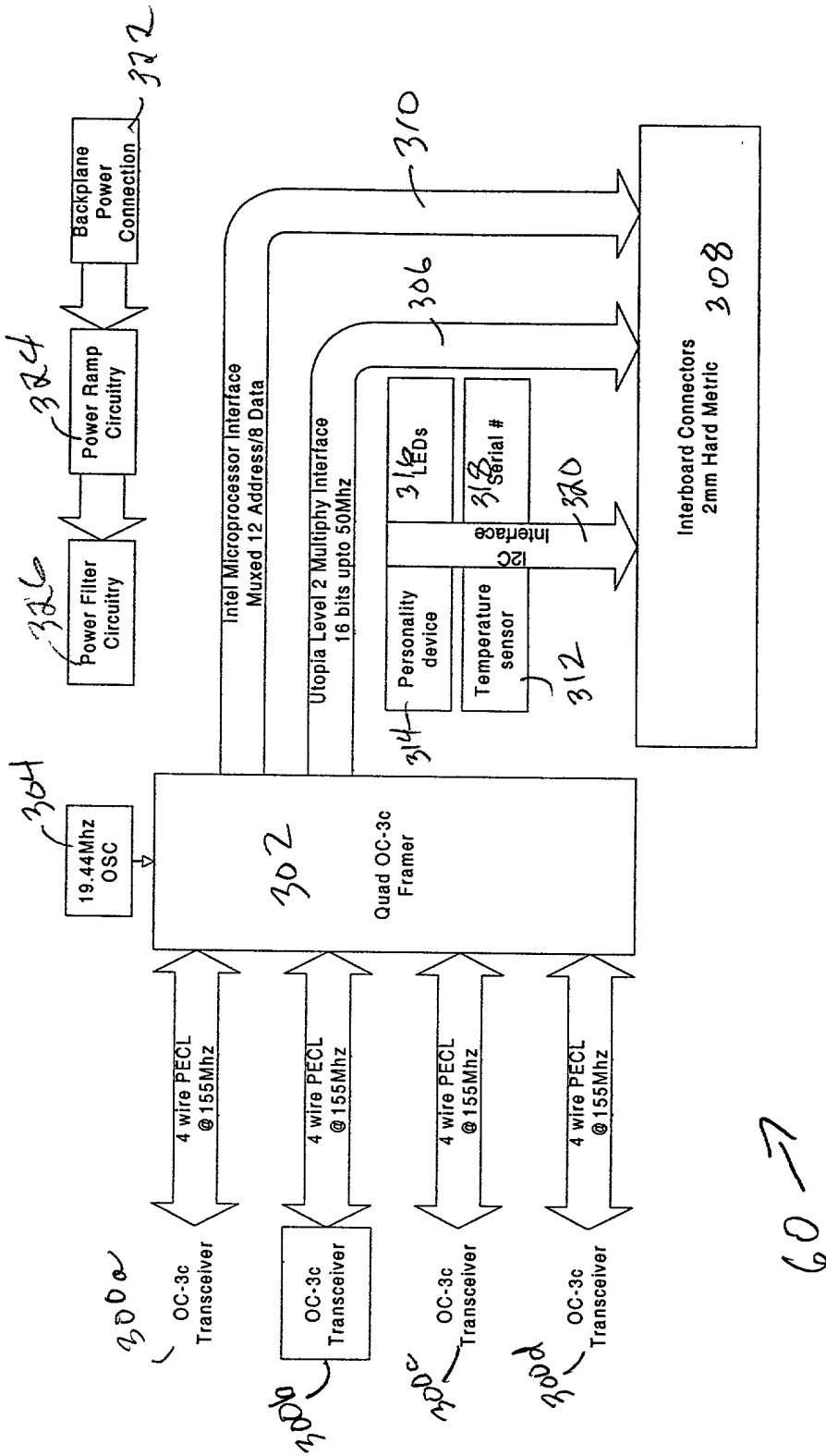
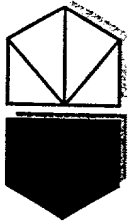


Fig. 5



# VLC - VDSL Line Card

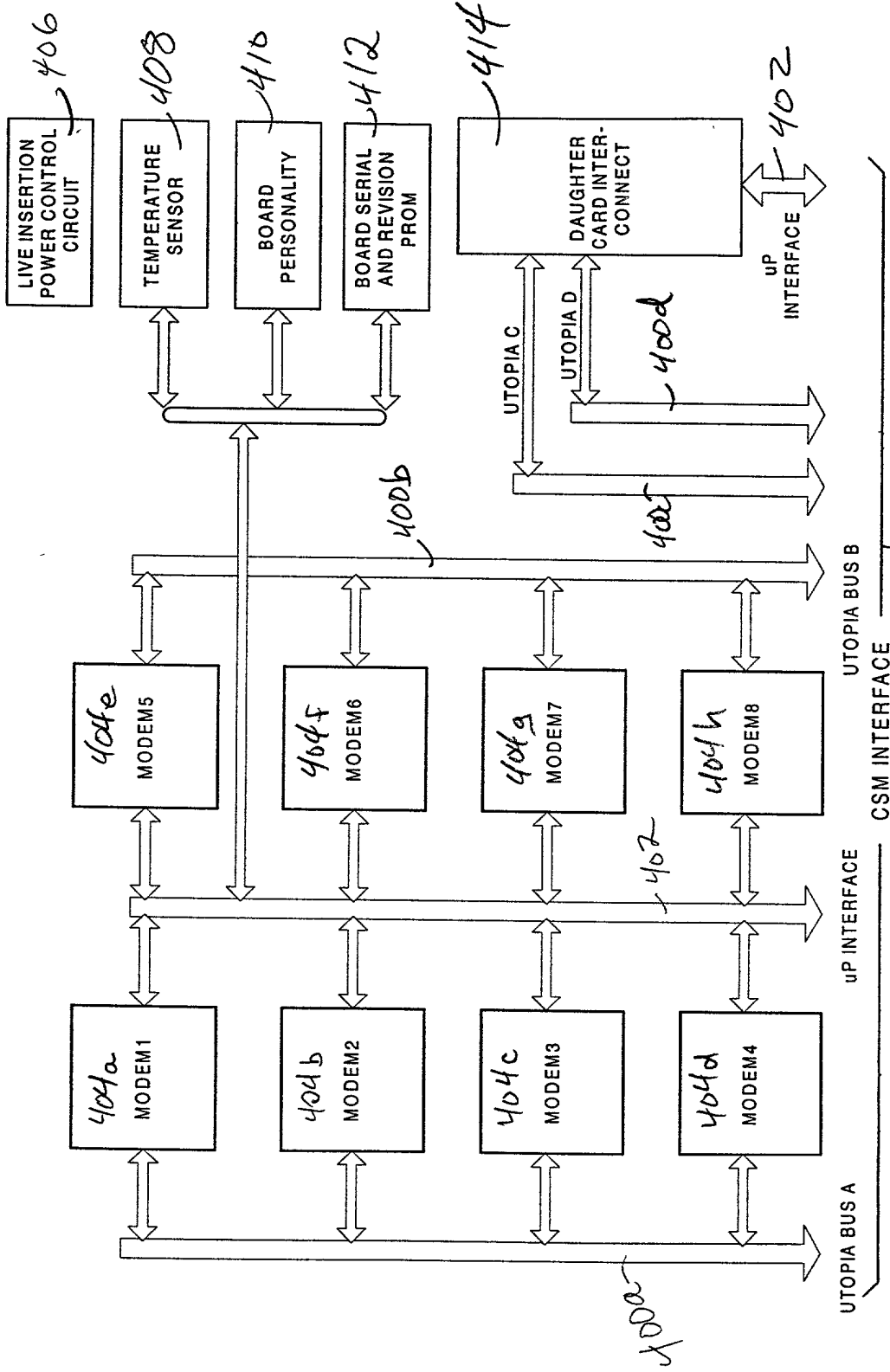
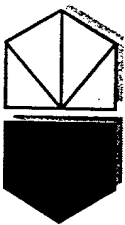


Fig. 6



# ViaJet A40x Block Diagram

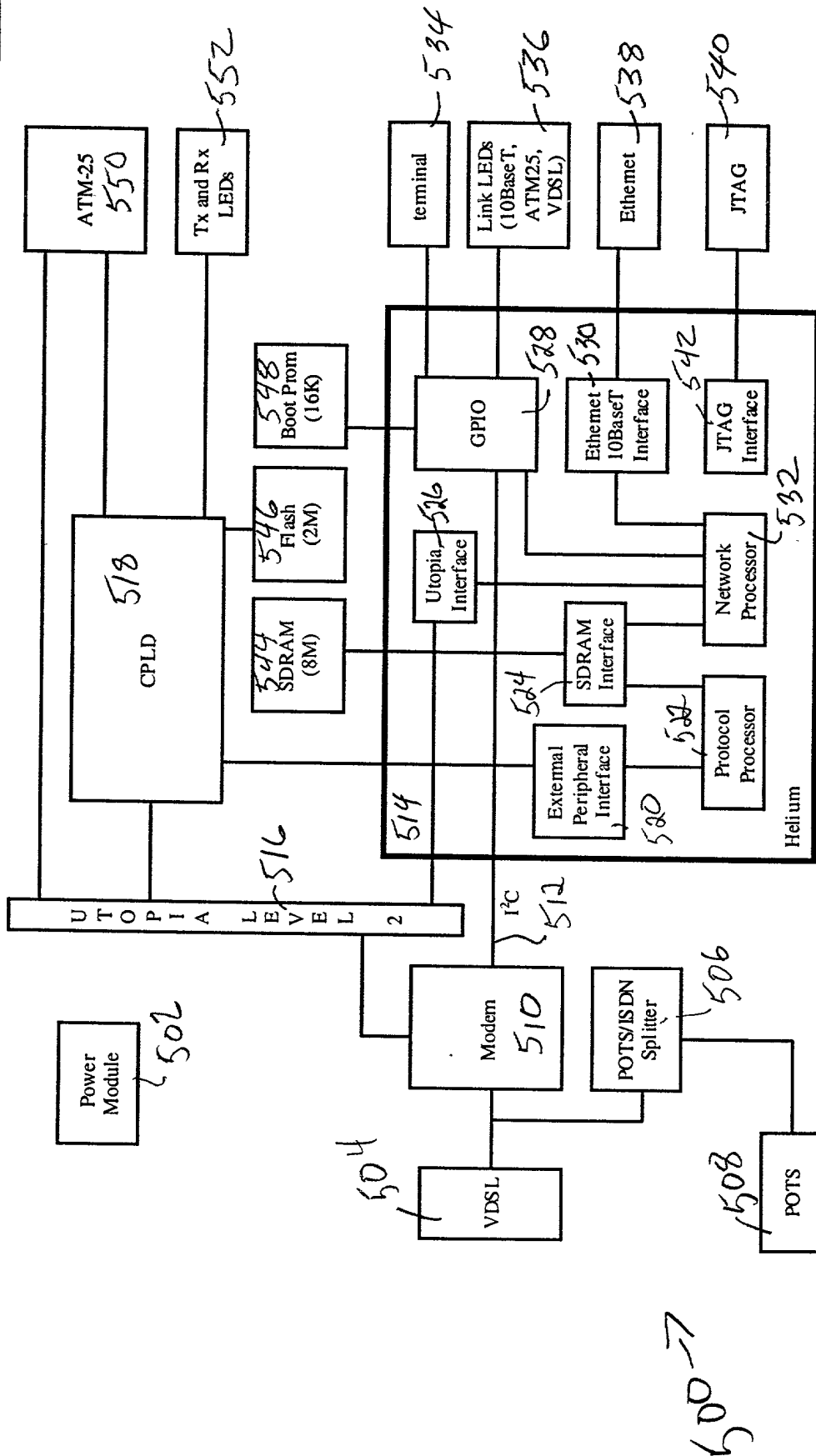


Fig. 1a



# ViaWay A406

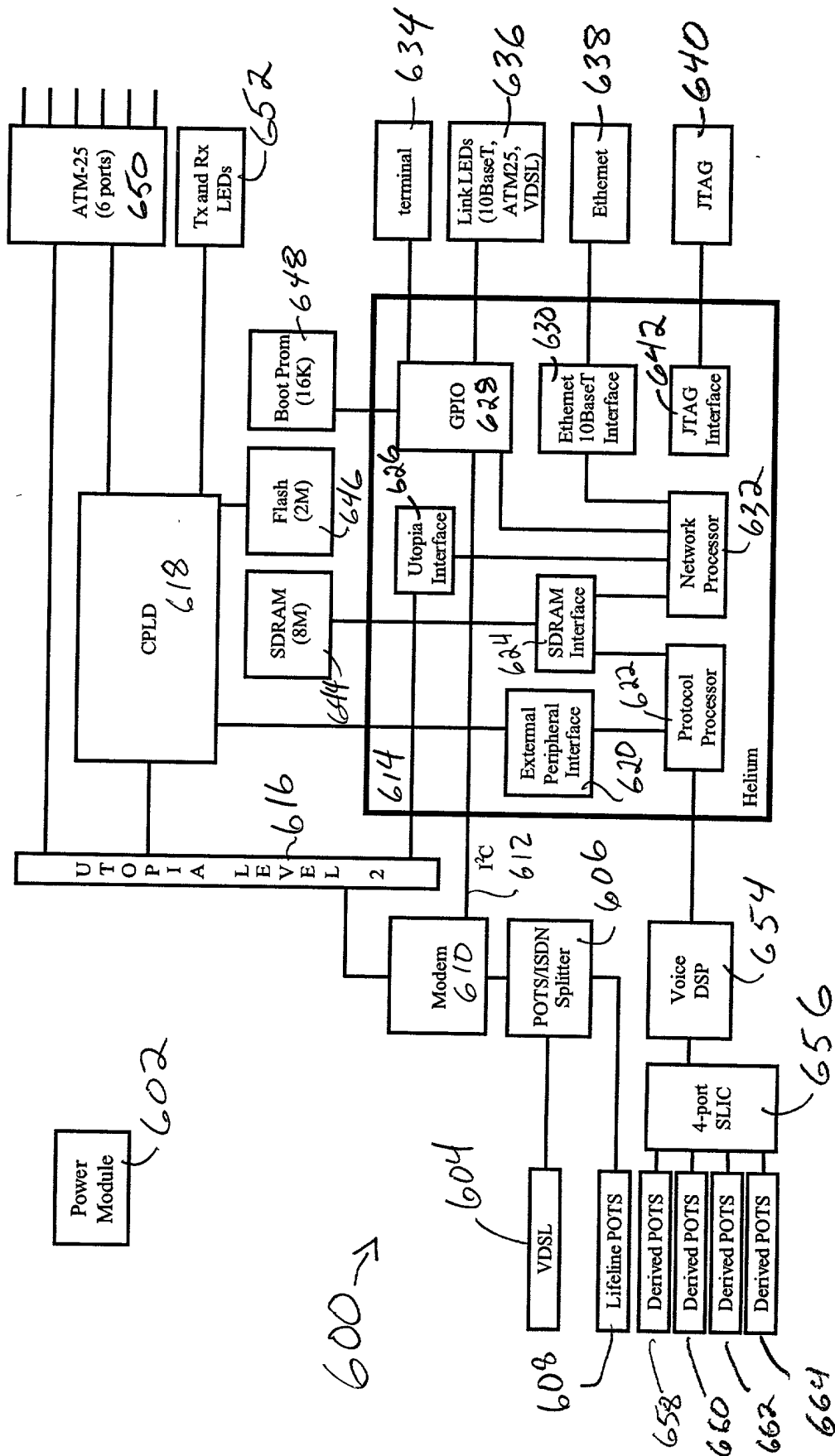
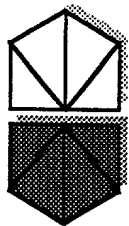


Fig. 7b





# Typical Digital Set-Top Box

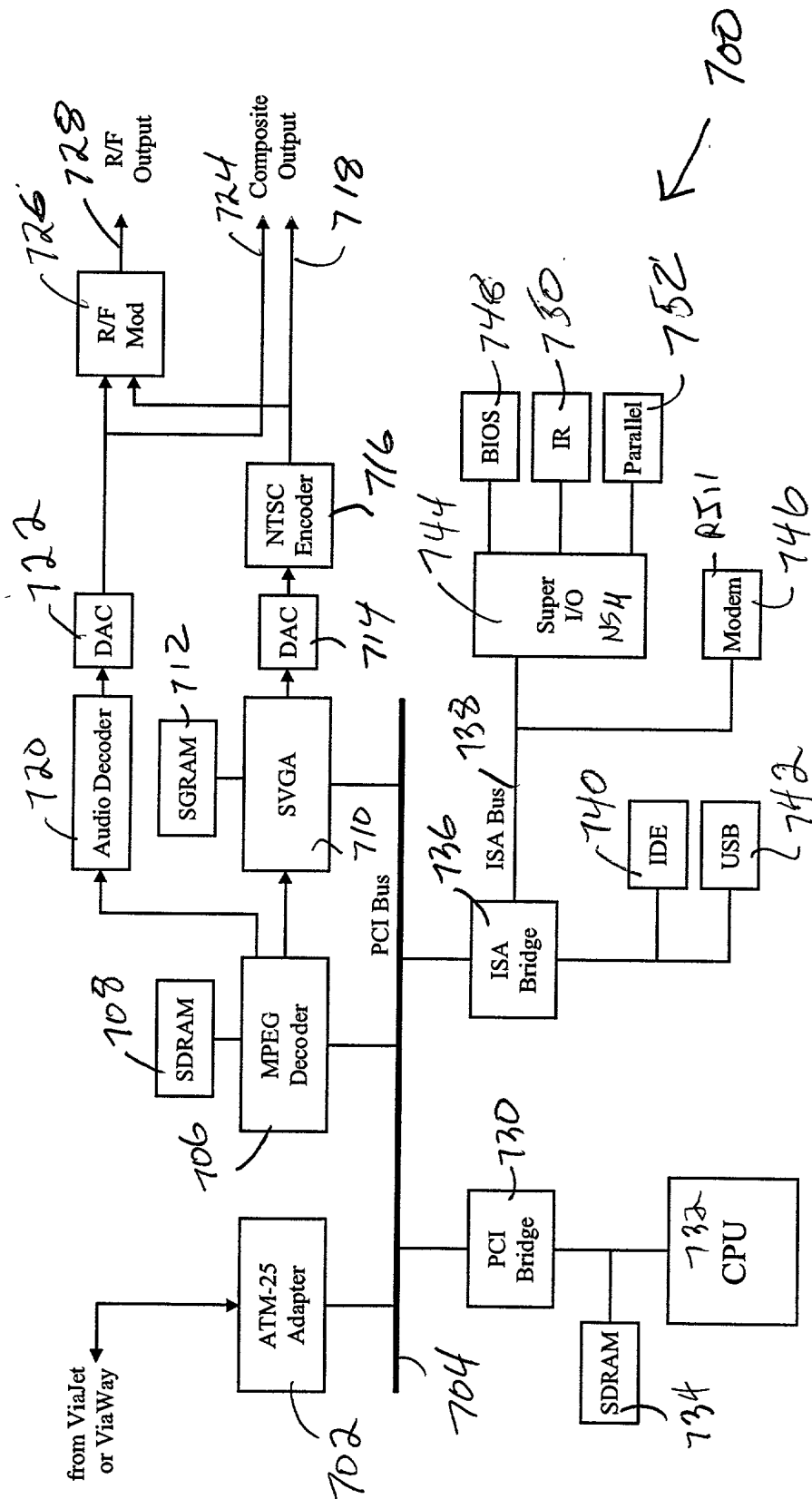


Fig. 7c

802

804

806

808

800

810

812

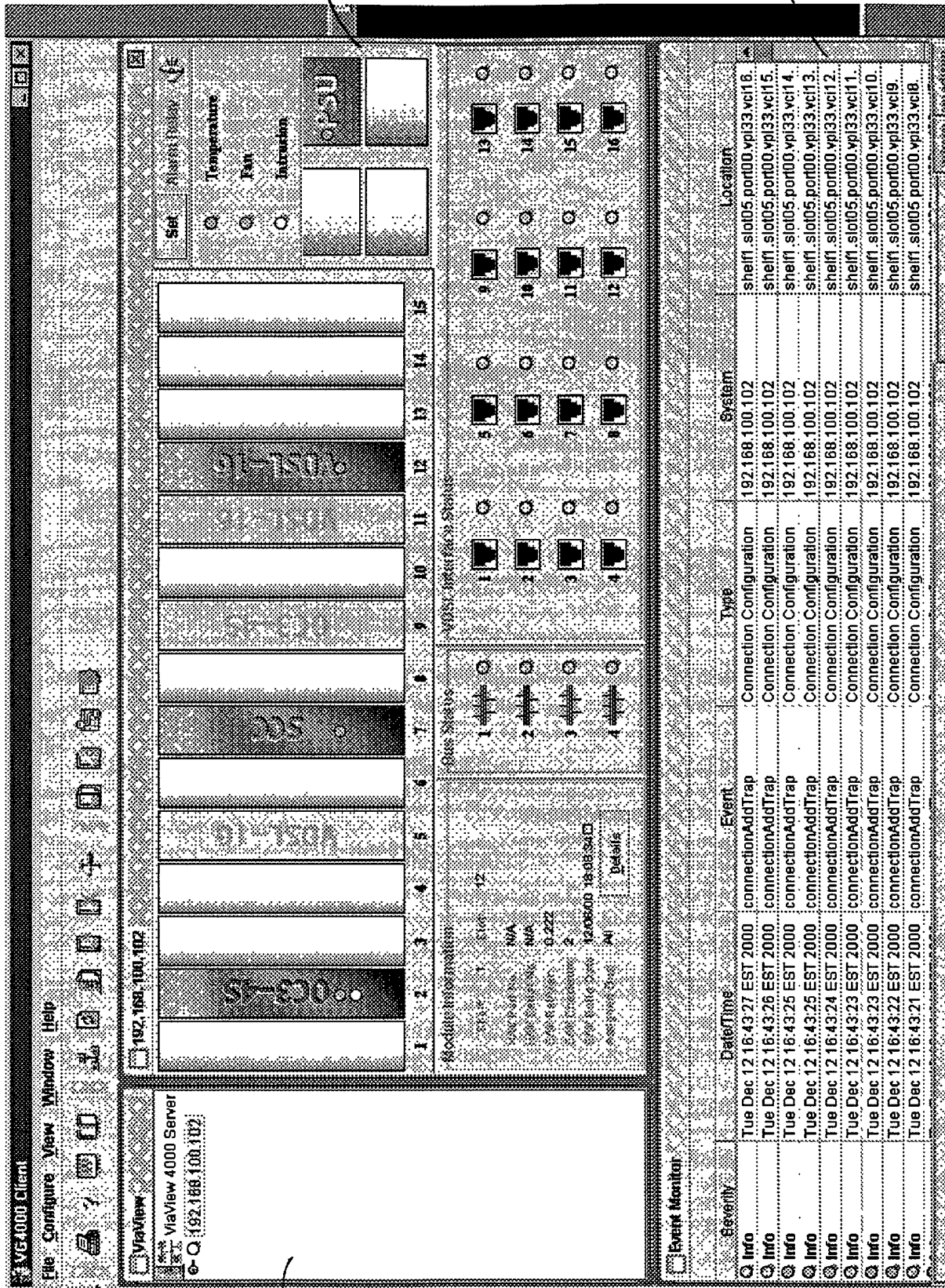
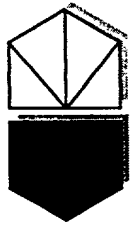
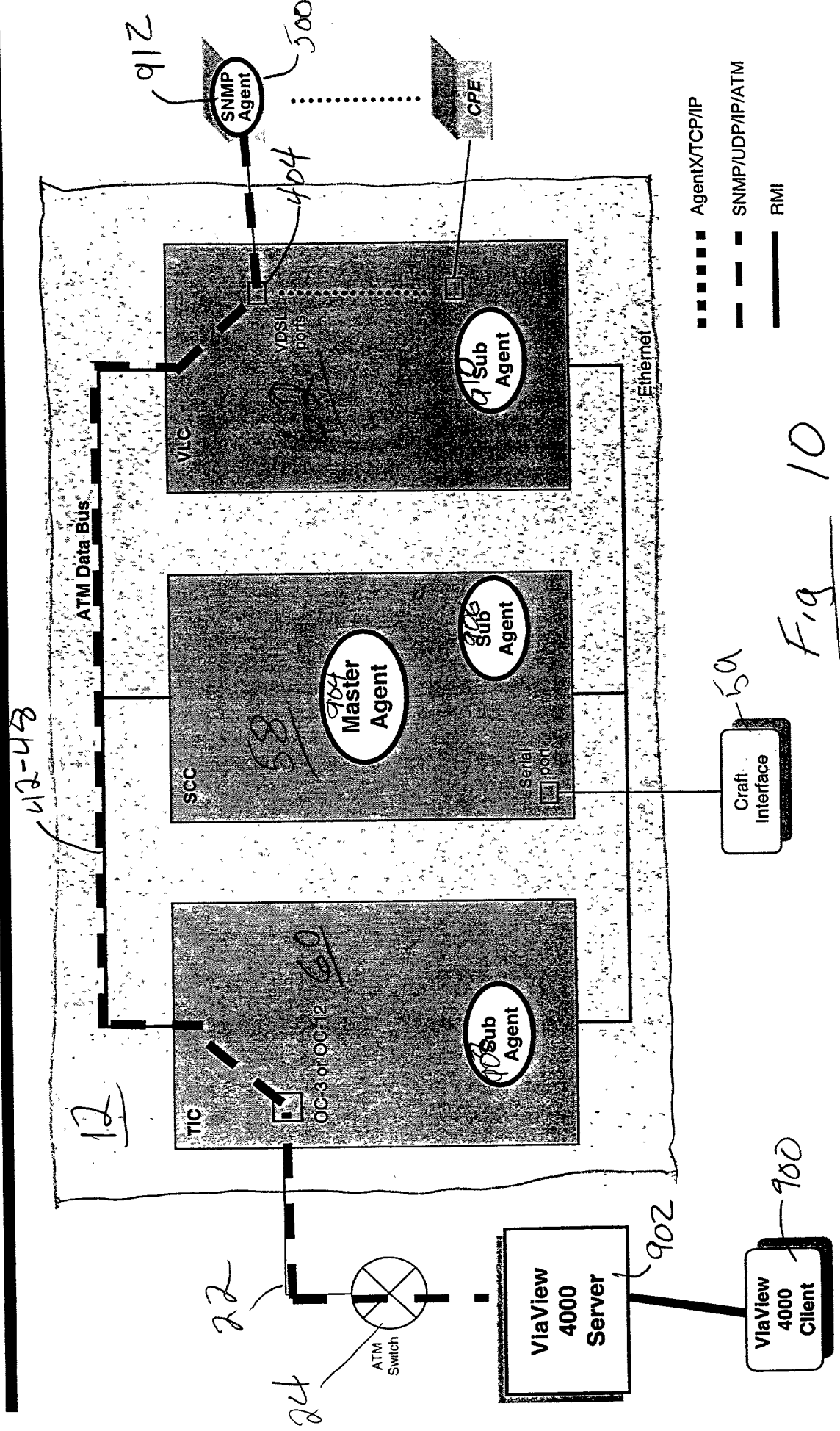


Fig. 8





# CPE Management Path





# Connection Management

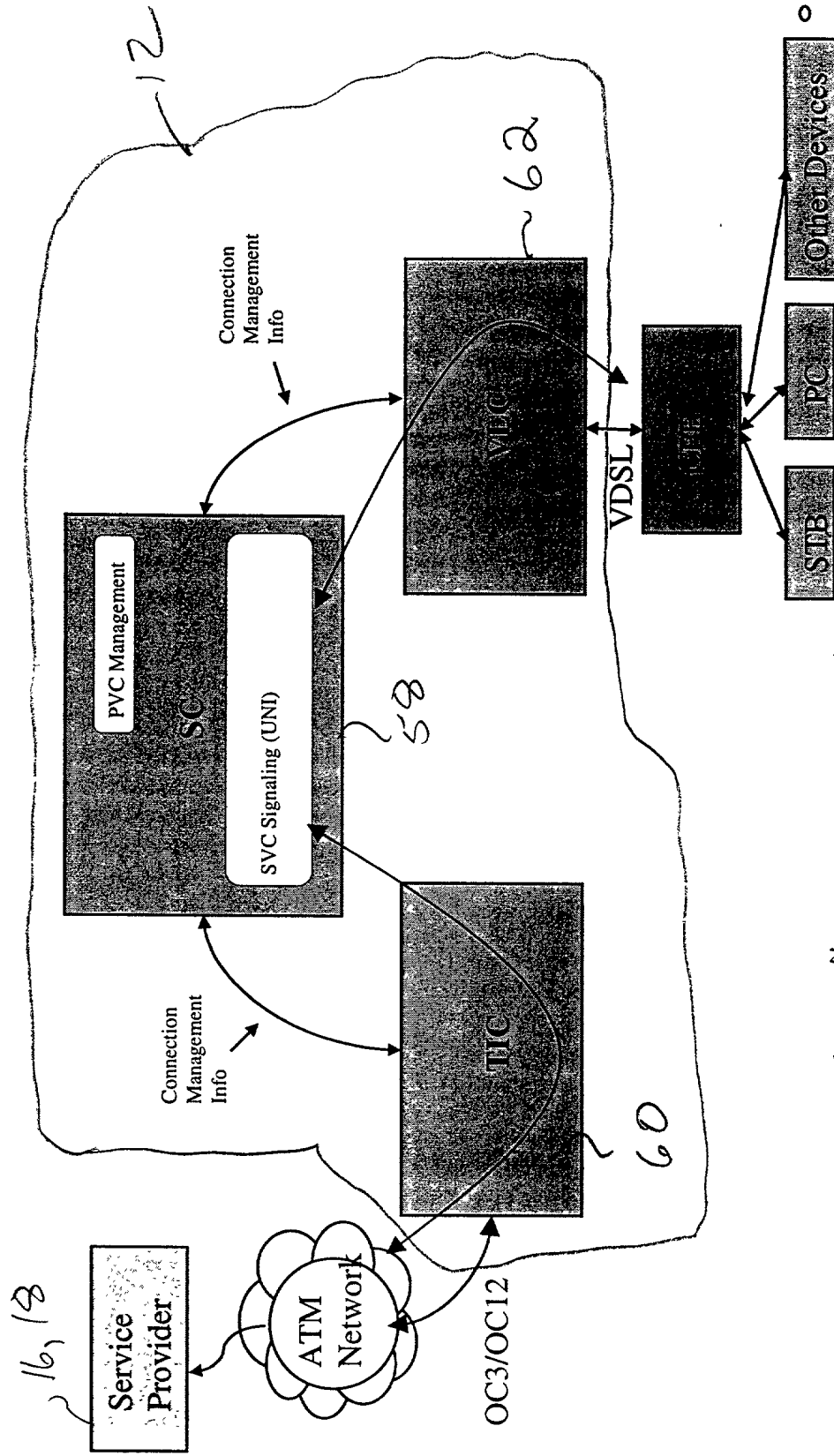
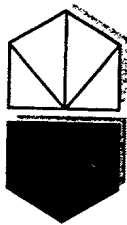


Fig. 11



# CATV/Broadcast Management

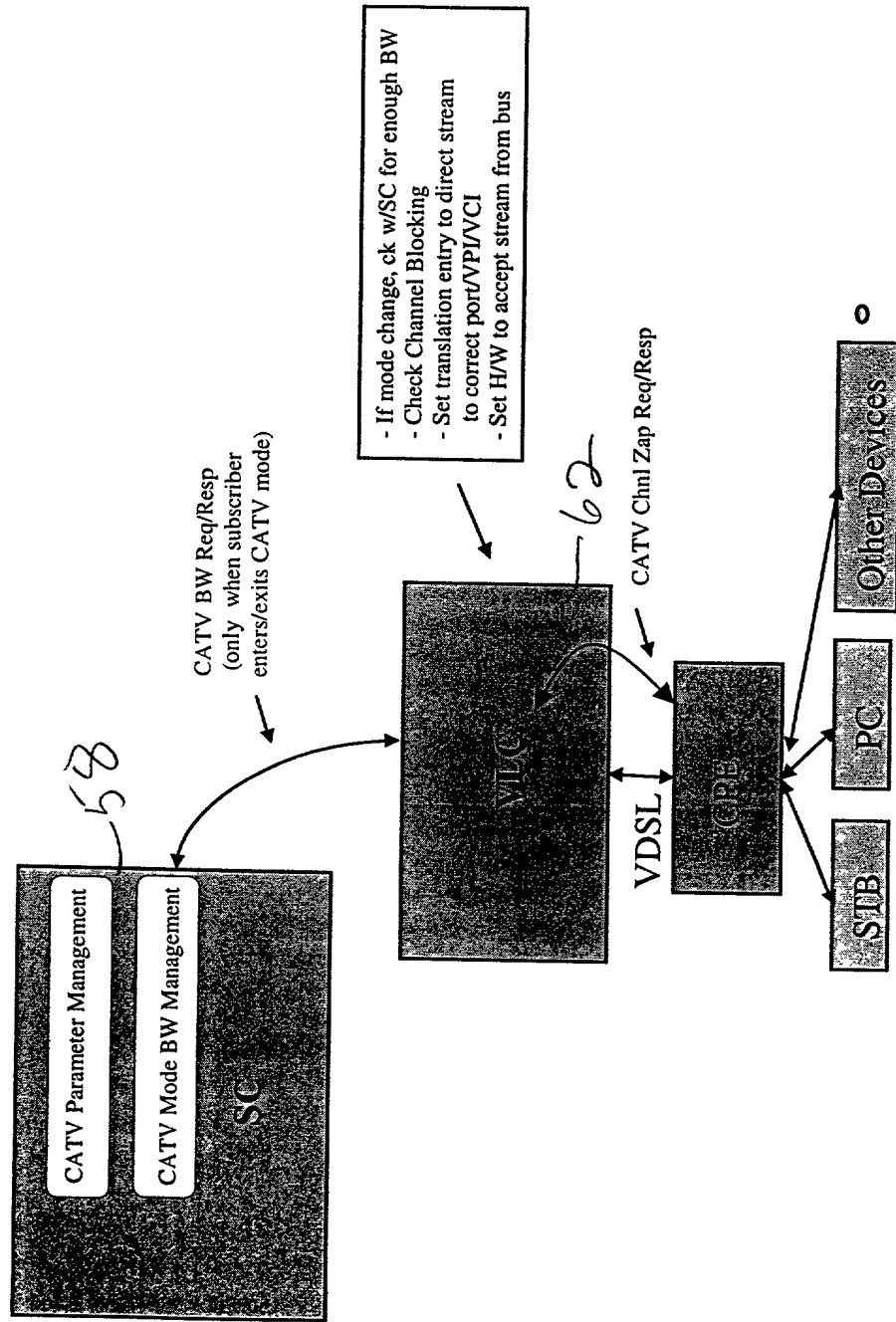


Fig. 12